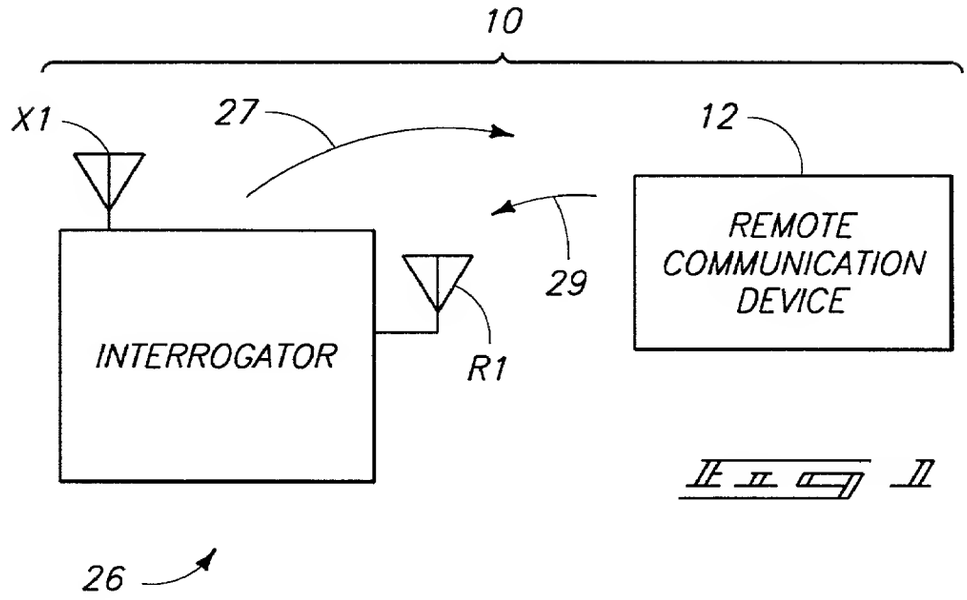

Fig 1

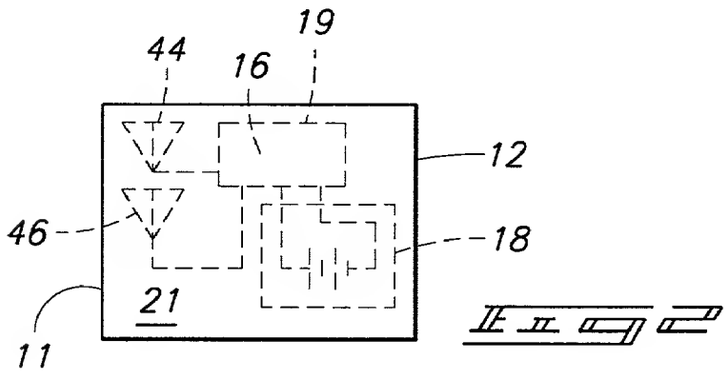

Fig 2

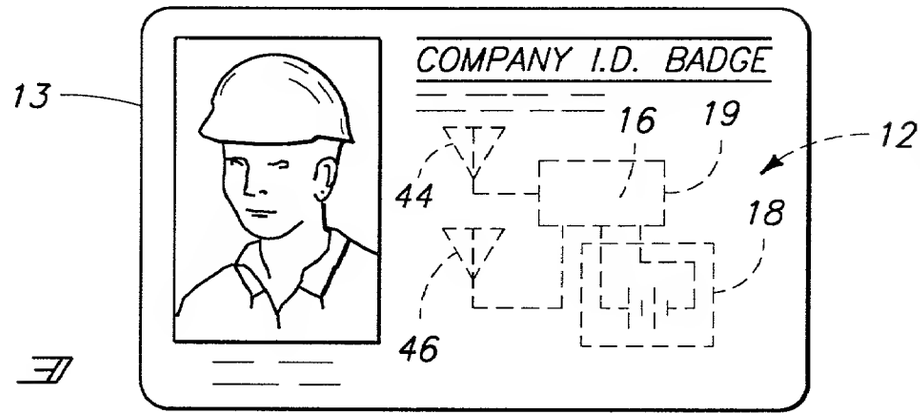

Fig 3

M440-321

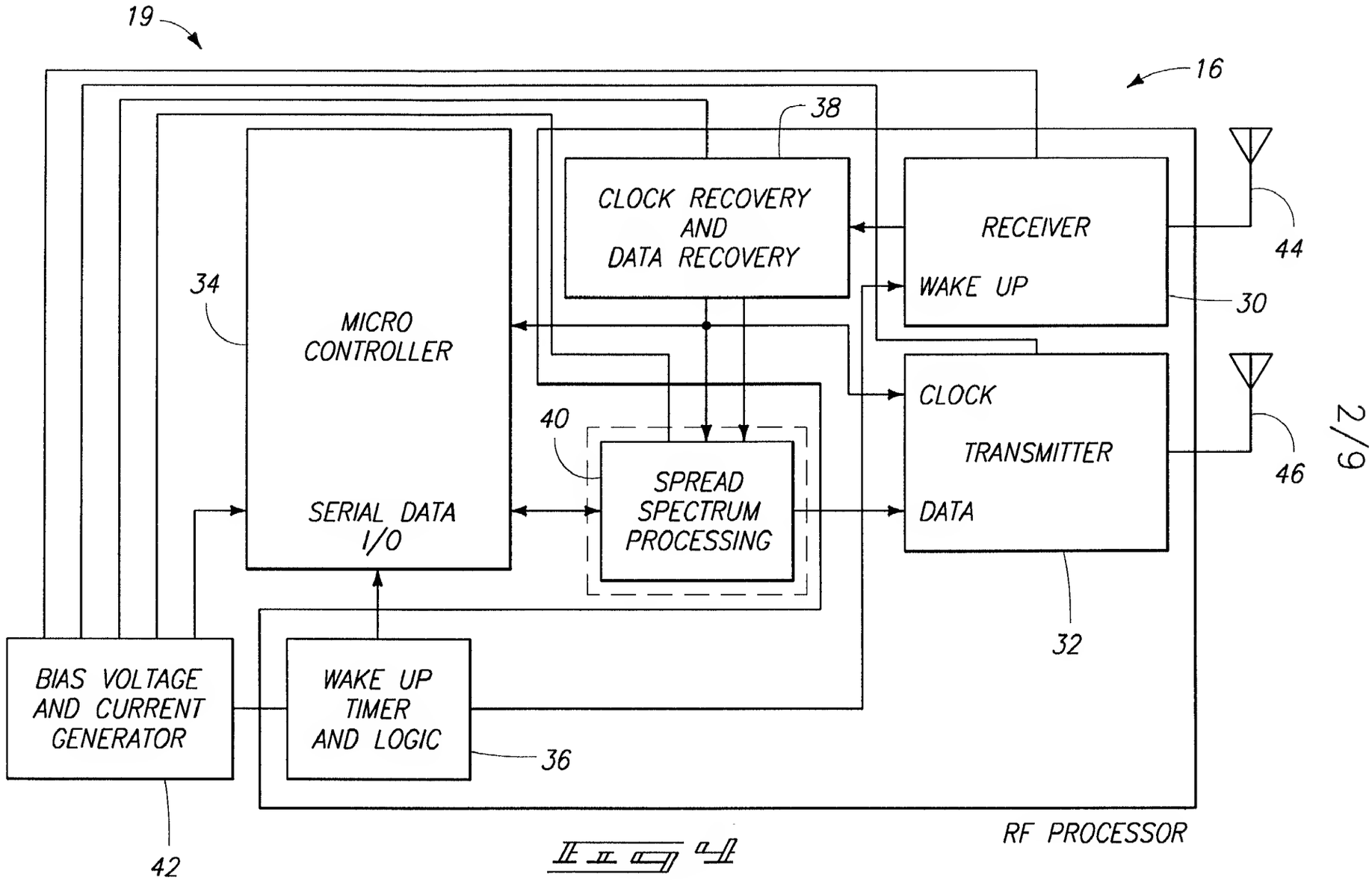

Fig. 1

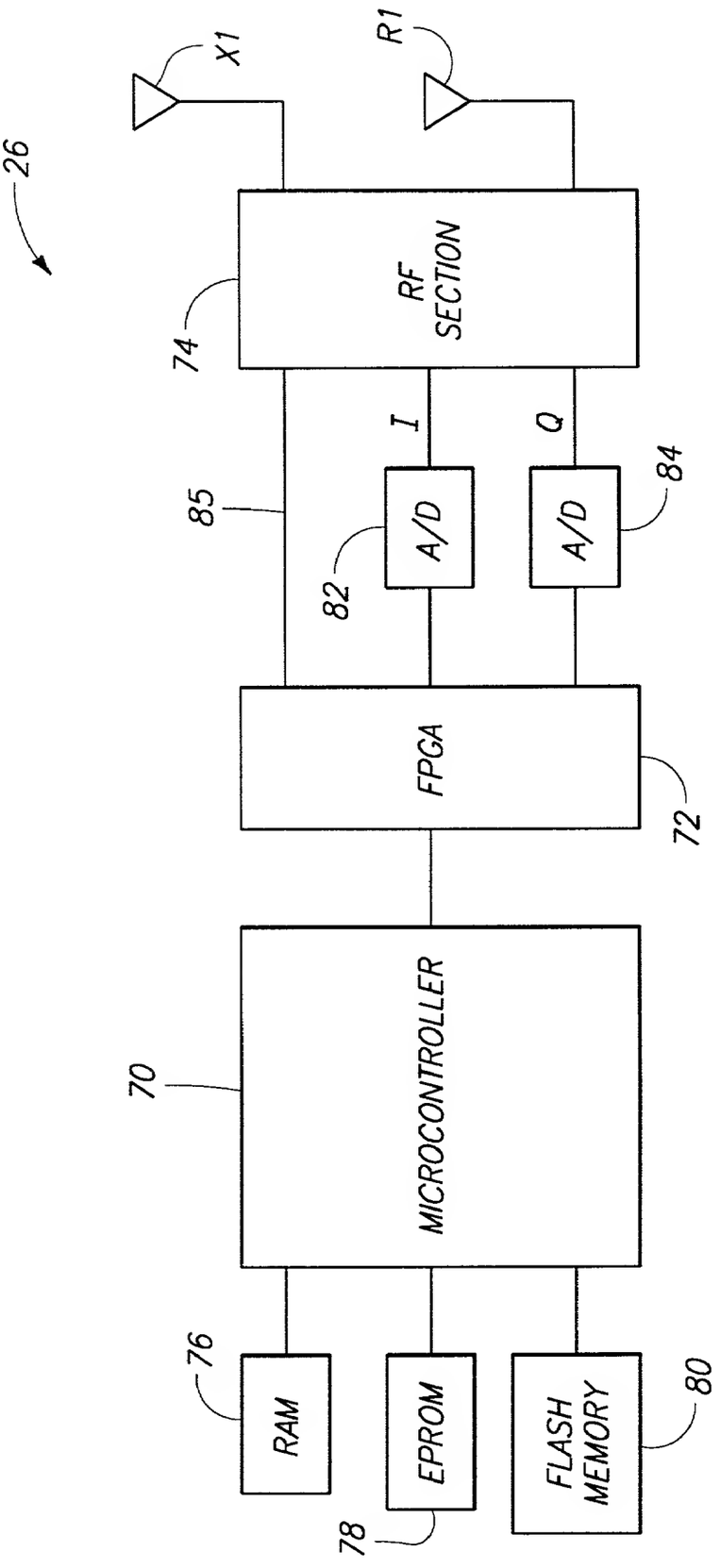
26
X1
R1
74
RF SECTION
I
Q
85
82
A/D
A/D
84
FPGA
72
70
MICROCONTROLLER
76
RAM
EPROM
78
FLASH MEMORY
80

Fig. 5

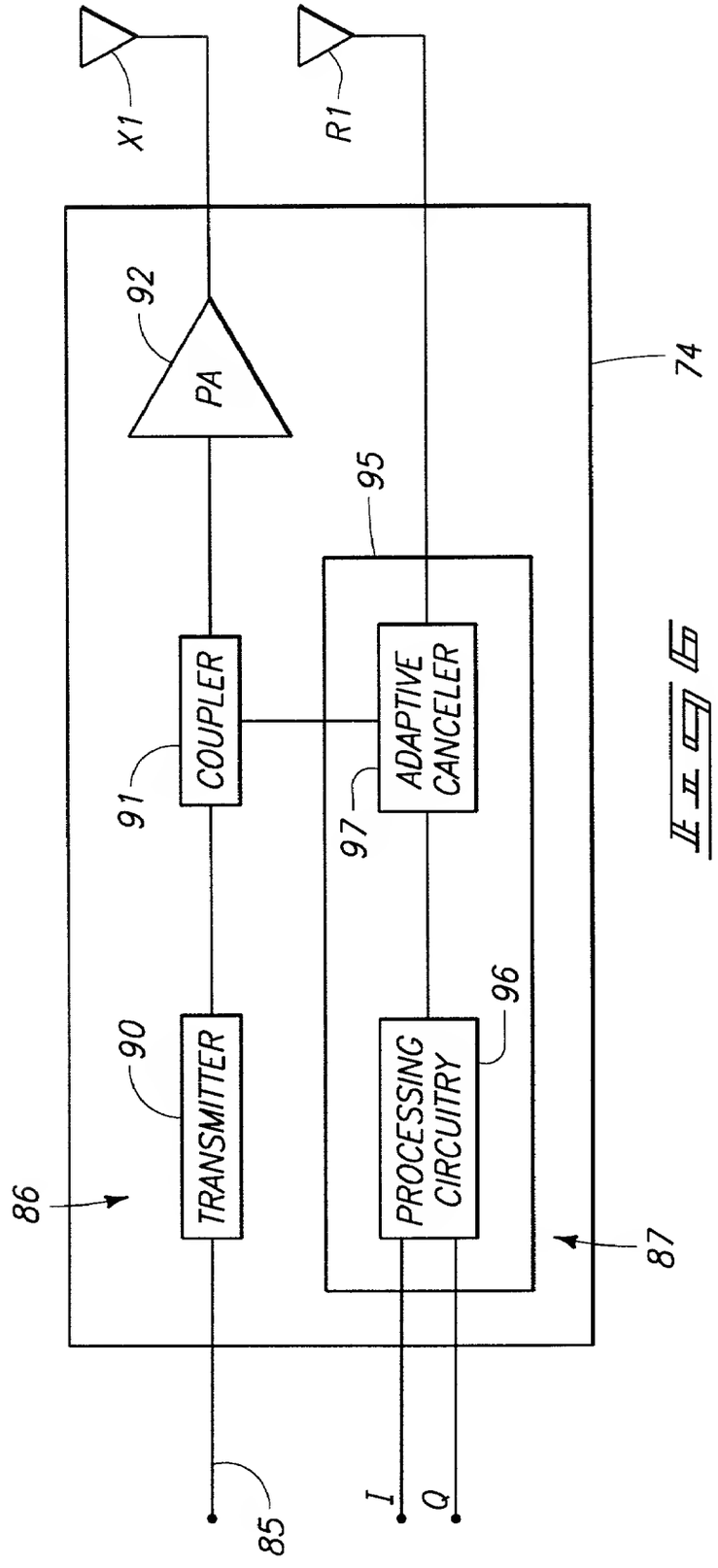
X1
R1
92
PA
74
95
COUPLER
91
ADAPTIVE CANCELER
97
TRANSMITTER
90
PROCESSING CIRCUITRY
96
86
87
85
I
Q

FIG 6

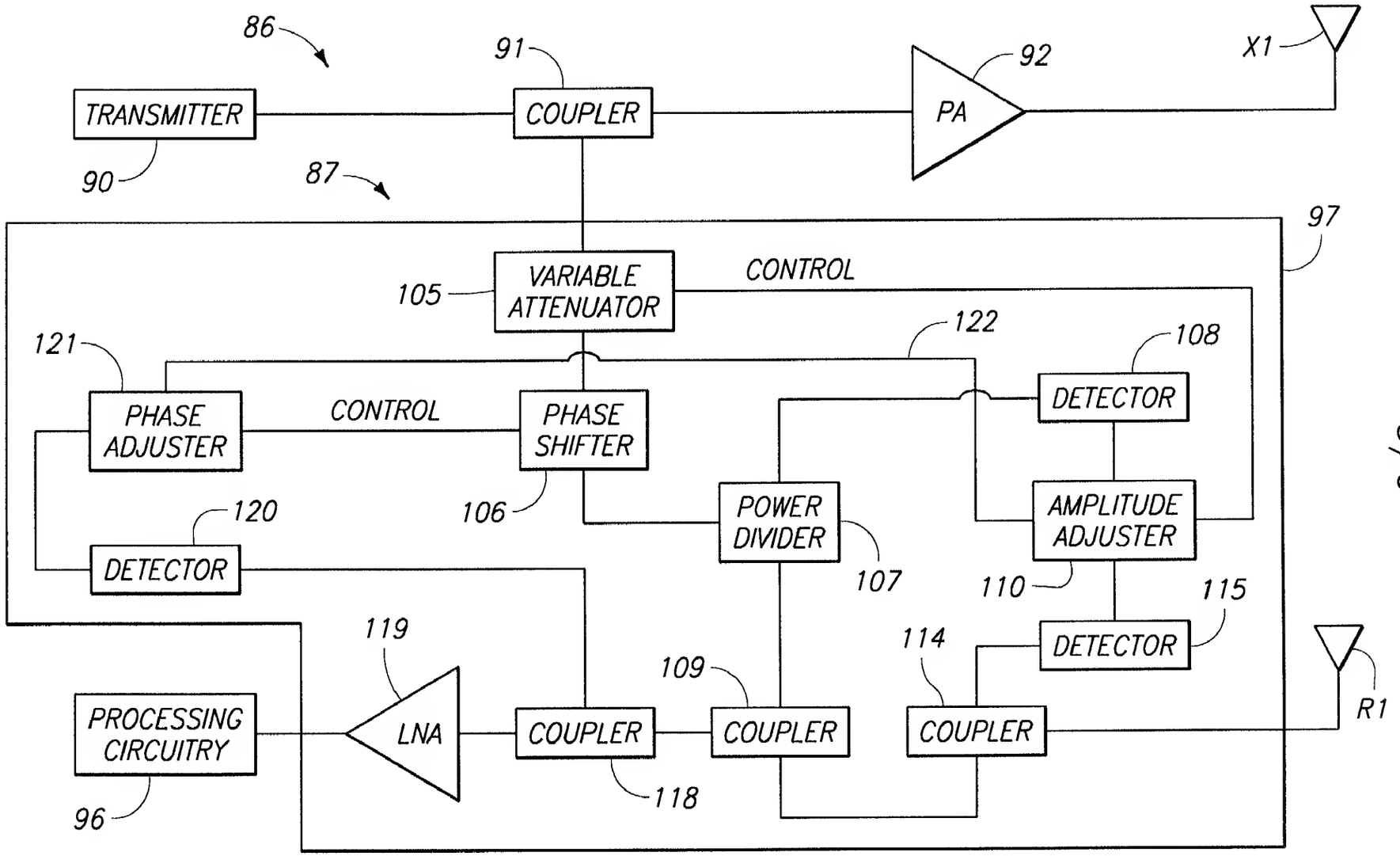
86
91
92
X1
TRANSMITTER
COUPLER
PA
90
87
97
105
VARIABLE ATTENUATOR
CONTROL
121
122
108
PHASE ADJUSTER
CONTROL
PHASE SHIFTER
DETECTOR
120
106
POWER DIVIDER
AMPLITUDE ADJUSTER
DETECTOR
107
110
115
119
114
109
PROCESSING CIRCUITRY
LNA
COUPLER
COUPLER
COUPLER
DETECTOR
R1
96
118

Fig 1

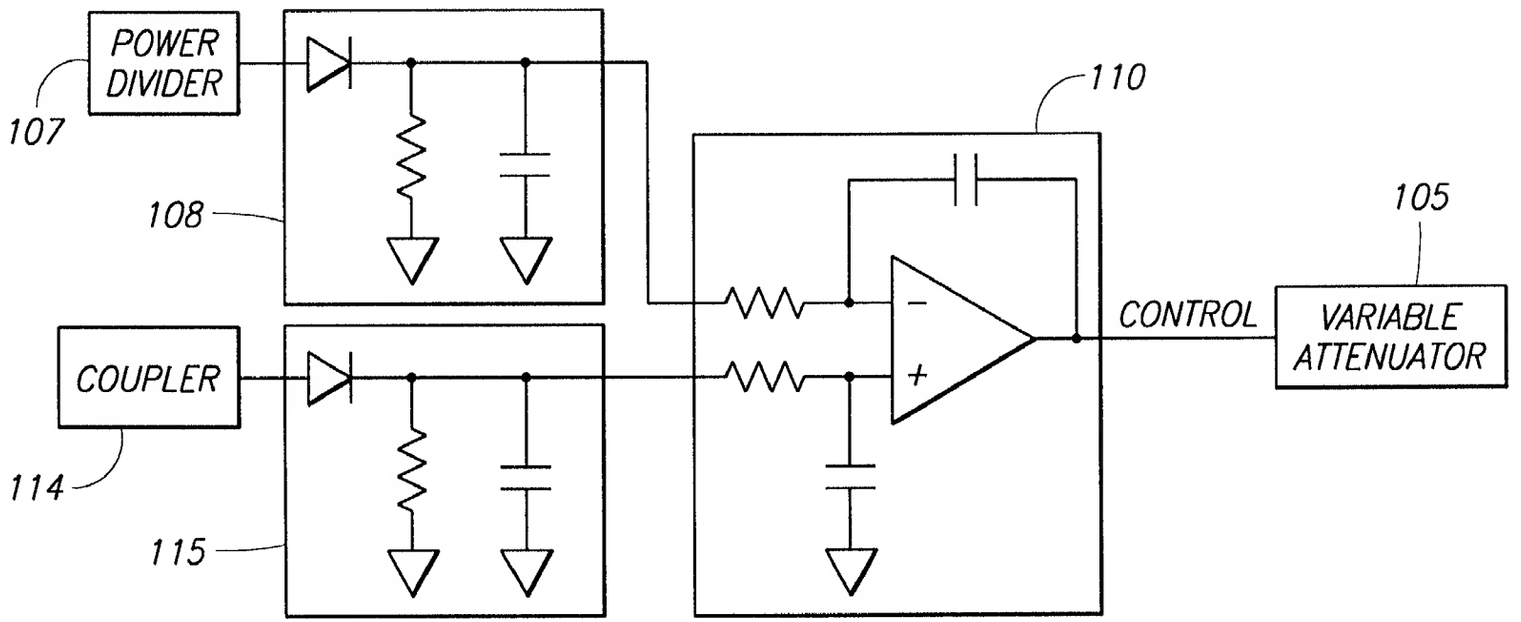
POWER DIVIDER
107
108
COUPLER
114
115
110
105
CONTROL
VARIABLE ATTENUATOR
−
+

Fig 8B

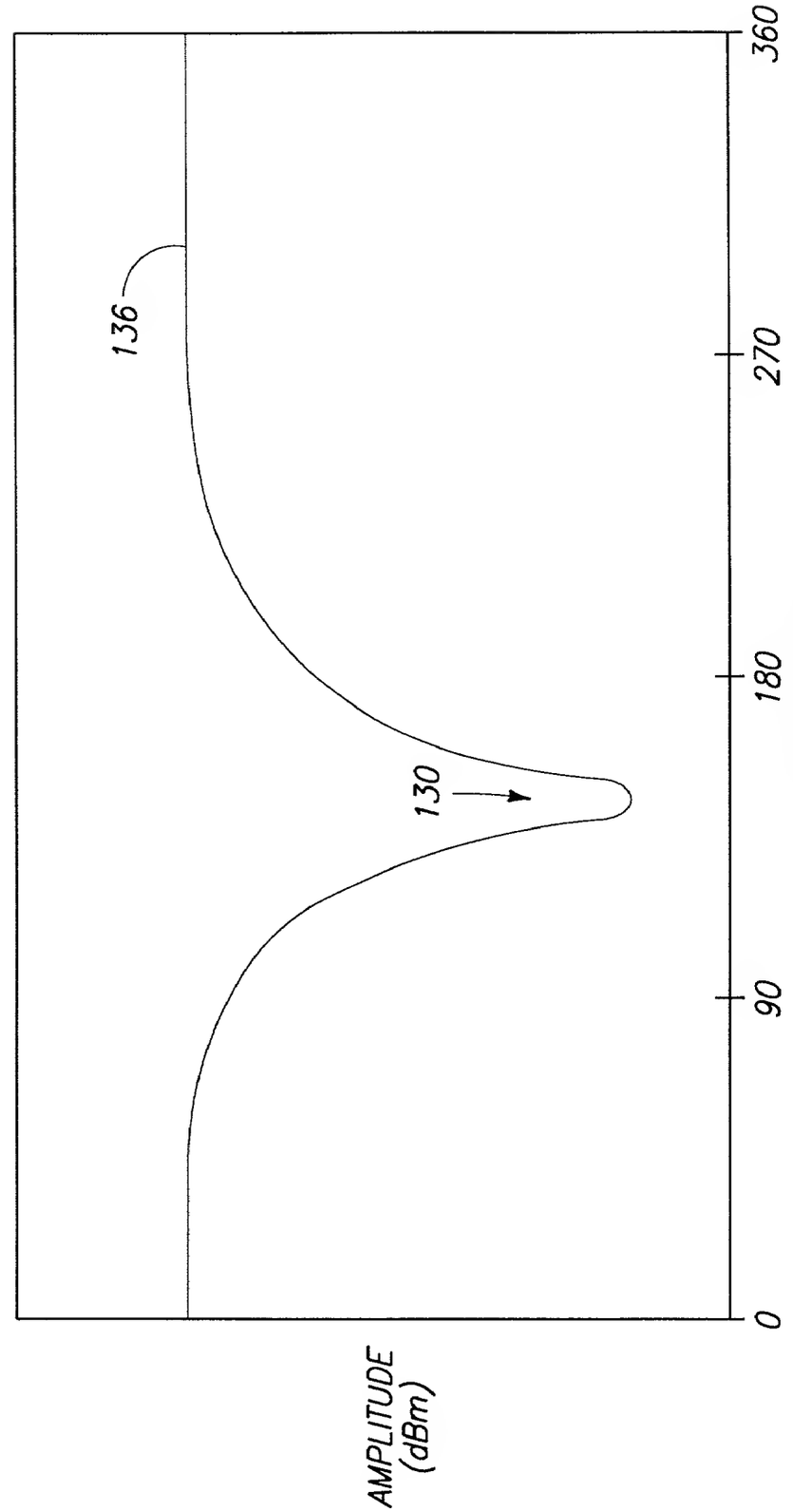

FIG 9

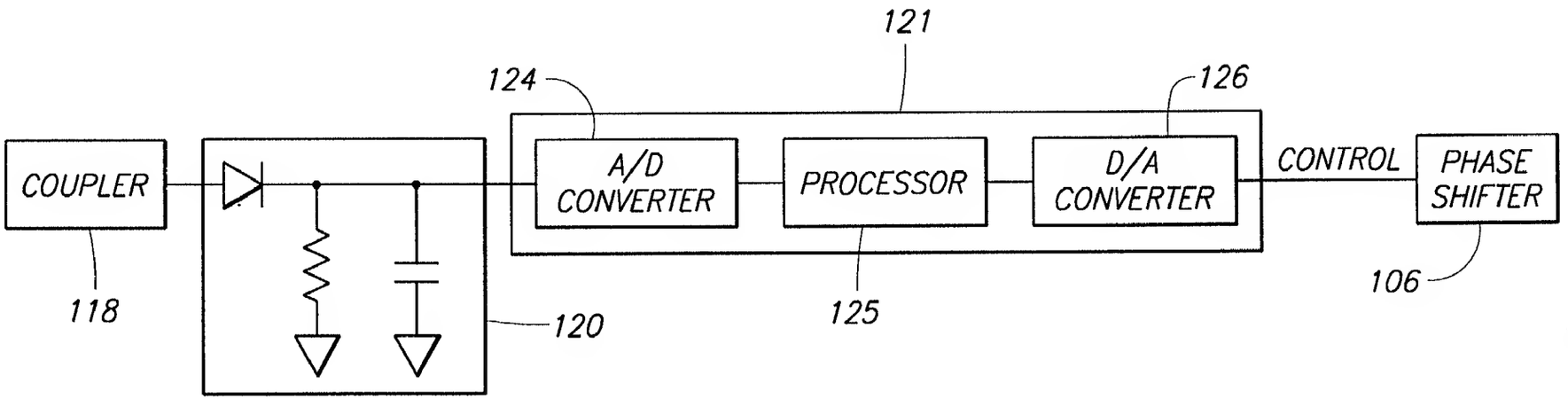
121
124
126
COUPLER
A/D CONVERTER
PROCESSOR
D/A CONVERTER
CONTROL
PHASE SHIFTER
118
120
125
106

Fig. 10

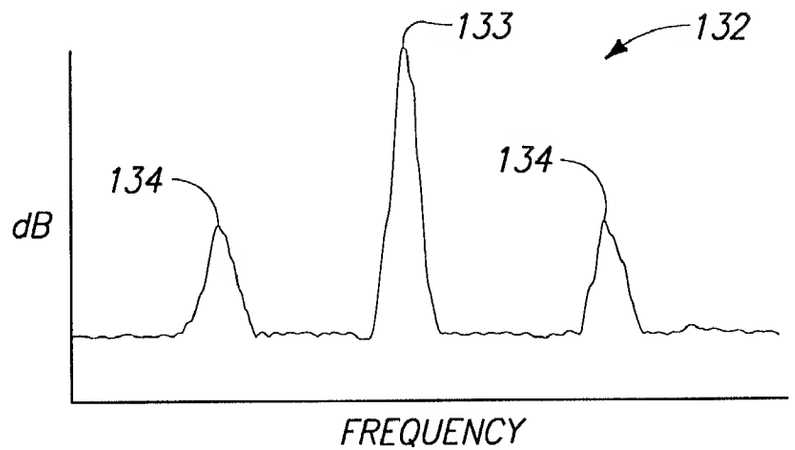

Fig. 11

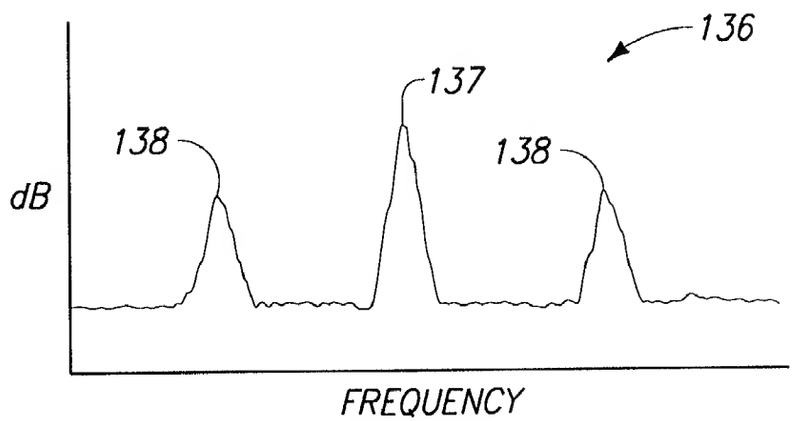

Fig. 12

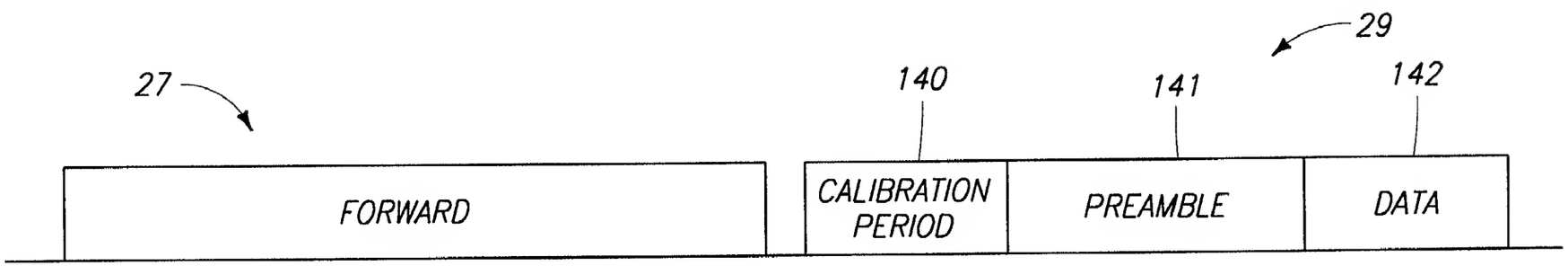

Fig. 13